

Publications

64. w/ Brion Shimamoto, "The Death of Microprocessors," *Embedded Systems Design*, Vol. 17, No. 9, September 2004, pp 16-20.
63. w/ Brion Shimamoto, "Reconfigurable Systems Emerge," *Field-Programmable Logic and Applications*, 14th International Conference Proceedings, FPL2004, August/September 2004, pp 2-11.
62. w/ Brion Shimamoto, "Microprocessor Sunset," *Microprocessor Report*, 3 May 2004, pp 1-4.
61. w/ Brion Shimamoto, "Go Reconfigure," *IEEE Spectrum*, Vol. 40, No. 12, December 2003, pp 36-40.
60. w/ Brion Shimamoto, "MEMS: Amazing Little Machines," *Embedded Systems Programming*, November, 2003, www.embedded.com/2003/0311.
59. w/Brion Shimamoto, "The Inevitability of Reconfigurable Systems," *ACM Queue*, Vol. 1, No. 7, October, 2003, pp 34-43.
58. w/ Brion Shimamoto, "The Rise of Reconfigurable Systems," *Proceedings of the International Conference on Engineering of Reconfigurable Systems and Algorithms*, Toomas Planks, Editor, June, 2003, pp 1-9.
57. "Technical Experts are in the Reserves," *Naval Institute Proceedings*, Vol. 128/6/1, 192, June 2002, pp 78-81.
56. w/Brion Shimamoto, "An Engineer's View of Venture Capitalists," *IEEE Spectrum*, September 2001, pp 13-19. <http://www.spectrum.ieee.org/WEBONLY/resource/sep01/speak.html>
55. w/ Steven Wallach, "Predicting the Future," *IEEE Micro*, Vol. 20, No. 4, July-August 2000, pp 88-87
54. w/ Brion Shimamoto, "Dynamic Logic for Portable Devices," *SSGRR 2000 Conference Proceedings*, August 2000, CD-ROM.
53. w/ Brion Shimamoto, "A Few Ideas That Could Change the World," *SSGRR 2000 Conference Proceedings*, August 2000, CD-ROM.
52. w/ Brion Shimamoto, "A Few Ideas That Could Change the World," published with "How Electronics Development is Changing in the U.S." *YRP Telecom Summit Conference Proceedings*, July 2000, pp 2-23.
51. "Death of the DSP," published online at TechnologyConnect.com and GilderTech.com, June 2000.
50. w/ Brion Shimamoto, "Embedded Systems and the Microprocessor," *Microprocessor Report*, Vol. 14 Archive 4, April 2000, pp 26-33.
49. w/ Brion Shimamoto, "Mercy, Mercy, Merced," *Microprocessor Report*, Vol. 13 No. 12, 13 Sep 1999, pp 20-22.
48. w/ CDR James Erickson, LCDR Dave Loar, LCDR Doug McBane, CDR Mike Pavlick, LCDR Mike Peek, and LT Joe Walker, "Impact of Joint Technical Architecture on Navy Acquisition," *Program Manager*, Vol. 27 No. 6, November-December 1998, pp 34-38.
47. "W(h)ither Intel," *Inside the New Computer Industry*, No. 114, Nov 1998, pp 1-5.
46. "W(h)ither Intel," *Microprocessor Report*, Vol. 12 No. 11, 24 Aug 1998, pp 14-16.

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45. "Get Ready for Reconfigurable Computing," *Computer Design*, Vol. 37, No. 4, April 1998, pp 55-63.
44. "This Cyberfarce Stars Elvis and the Nerds," *Computer*, Vol. 31, No. 4, Apr 1998, pp 104-105.
43. "Of nerds and kings," *EE Times*, 18 Nov 1996, pp 52.
42. "Microprocessor-Based Computers," *Computer*, Vol. 29, No. 10, Oct 1996, pp 27-37.
41. "The Case for Reconfigurable Computing," *Microprocessor Report*, Vol. 10 No. 10, 5 Aug 1996, pp 25-27.
40. "Intel Will Remain on Top, as Pundits Remain Wrong," *EE Times*, 11 Dec 1995, pp 30.
39. "What Killed the MPU?" *EE Times*, 11 Dec 1995, pp 30.
38. "Technology and Business: Forces Driving Microprocessor Evolution," *Proceedings of the IEEE*, Vol. 83, No. 12, Dec 1995, pg 1641-1652.
37. "The Future for Programmable Logic," *Dr. Dobb's Journal*, Vol. 20 No. 7, Jul 1995, pp 60-68.
36. "Reconfigurable Hardware in Computer Systems," *Proceedings Design SuperCon 95: Management Design Conference*, 28 Feb 1995, Chapter 7, pp 1-18.
35. "Power Play: Year Two," *Microprocessor Report*, Vol. 8 No. 12, 12 Sep 1994, pp 18-21.
34. "Intel and HP Shoot Each Other in the Foot," *Microprocessor Report*, Vol. 8 No. 11, 22 Aug 1994, pp 16-17.
33. w/ Bob Beachler "Reconfiguration Showing Promise," *Electronic Engineering Times*, No. 792, 11 Apr 1994, pp T42-43.
32. "Microprocessors Hit the Performance Wall (Again)," *Dr. Dobb's Journal*, Vol. 19 No. 1, Jan 1994, pp 34.
31. "Computer Science and the Microprocessor," *Dr. Dobb's Journal*, Vol. 18 No. 6, Jun 1993, pp 18-32.
30. "Microprocessors," *American Institute of Physics Handbook*, Vol. 10, 1994, pp 297-319.
29. "The PowerPlay," *Microprocessor Report*, Vol. 7 No. 1, 25 Jan 1993, pp 25-26.
28. "The Business of Microprocessors: Beyond the Folklore," *Embedded Systems Conference Proceedings*, Vol. I, 21-24 Sep 1992, pp 5-19. Keynote address.
27. "Die Like a Man," *Microprocessor Report*, Vol. 6 No. 6, 6 May 1992, pp 18-21.
26. "The Beauty of Microprogramming for VLSI," *Some Microprogramming Textbook*, Milutinovic, Prentice-Hall, 1992, pp xx-yy.
25. "Football and Microprocessors," *Microprocessor Report*, Vol. 6 No. 1, 22 Jan 1992, pp 19-21.
24. "MIPS and Sunset," *Microprocessor Report*, Vol. 5 No. 12, 26 Jun 1991, pp 12-17.
23. "A Vision of ACE and the Future of Intel," *Microprocessor Report*, Vol. 5 No. 10, 29 May 1991, pp 12-13.
22. "1991: The Year of the RISC," *RISC Management*, Issue 33, Jun 1991, pp 1-5.
21. "1991: The Year of the RISC," *Microprocessor Report*, Vol. 5 No. 2, 6 Feb 1991, pp 3, 16-17.
20. "Clean Room Engineering: How to Copy the Idea without Violating Intellectual Property Rights," *TekBriefs*, November/December 1990, pp 8-10.
19. "Legal Bu\$ine\$\$," *TekBriefs*, November /December 1990, pp 11-12.

18. "My Klone86 Will Be Out Very Soon," *Microprocessor Report*, Vol. 4 No. 17, 3 October 1990, pp 3,17.
17. "Legal Bu\$ine\$\$," *Microprocessor Report*, Vol. 4 No. 10, 30 May 1990, pp 3,17.
16. "It's Not RISC vs. CISC—It's New vs. Old," *Microprocessor Report*, Vol. 3 No. 2, February 1989, pp 12-16.
15. "Experiences in Commercial Microprocessor Design," *Microprocessors and Microsystems*. Vol. 12 No. 8, October 1988, pp 419-432.
14. *Microprocessor Logic Design*. Bedford, MA: Digital Press, 1987. (With Brion Shimamoto)
13. "Trends in Commercial VLSI Microprocessor Design," *VLSI CAD Tools and Applications*. Eds. Wolfgang Fichtner and Martin Morf. Hingham, MA: Kluwer Academic Publishers, 1987.
12. w/ Dick Hadsell "Micro/370 Control Flow," IEEE International Conference on Computer Design. October 1986, pp 332-335.
11. C. A. Trempe, R. W. Hadsell, M.-Y. Tsai, H. L. Tredennick, and H. H. Chao. "Special Function Unit for the Micro/370 Microprocessor," IEEE International Conference on Computer Design. October 1986, pp 398-401.
10. "The Impact of VLSI on Microprogramming," Keynote Address, *Proceedings of the 19th Annual Microprogramming Workshop*. October 1986, pp 2-5.
9. w/ M. Y. Tsai "Structured Physical Design for the Micro/370 Microprocessor," IBM Structured Logic Symposium. November 1982.
8. "The 'Cultures' of Microprogramming," *Proceedings of the 15th Annual Microprogramming Workshop*. October 1982, pp 79-83.
7. w/ Brion N. Shimamoto. "On Systematic Generation of Scientific Papers," *IEEE Transactions on Professional Communication*, Vol. PC-24, No. 3. September 1981, pp 124-127.
6. "How to Flowchart for Hardware," *IEEE Computer*. December 1981, pp 87-102.
5. w/ John Zolnowsky "Design and Implementation of System Features for the MC68000," *Proceedings of Compton, Fall 1979*. September 1979, pp 2-9.
4. "Implementation Decisions for the MC68000 Microprocessor," *Proceedings of the 3rd Rocky Mountain Symposium on Microcomputers: Systems, Software, Architecture*. August 1979.
3. w/ Skip Stritter "Microprogrammed Implementation of a Single Chip Microprocessor," *Proceedings of the 11th Annual Microprogramming Workshop*. November 1978, pp 8-16.
2. w/ T. A. Welch. "High-Speed Buffering for Variable Length Operands," *Proceedings of the 4th Annual Symposium on Computer Architecture*. March 1977, pp 205-210.
1. w/ T. A. Welch. "An Operand Address Generator for a Variable Word Length Processor," *Proceedings, IEEE Region 5 Conference*. April 1976, pp 247-252.